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B/eko
J. Debbas

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION OF:

JAMES PRIETO, JR.
STUART T. LINSKY

SERIAL NO.: Not yet assigned.

FILED: Herewith

FOR: METHOD AND APPARATUS FOR
REAL TIME DOWN-LINK
SCHEDULING IN A SATELLITE
COMMUNICATIONS NETWORK



INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sirs:

Pursuant to 37 C.F.R. § 1.56, it is respectfully requested that the following reference(s) be considered in the examination of the above-identified patent application:

USP 5,889,779 to Lincoln

USP 5,974,465 to Wong

IEEE Communications Magazine, "Hardware Implementation of Fair Queuing Algorithms for Asynchronous Transfer Mode Networks", Varma and Stiliadis, Pg. 54 et seq (December 1999).

Proceedings of the IEEE, "Service Disciplines for Guaranteed Performance Service in Packet-Switching Networks", Zang, Vol. 83, No. 10, Pg. 1374 et seq (October 1999).

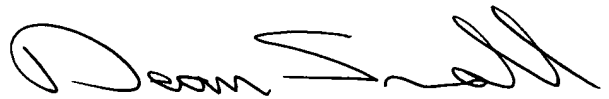
Proceedings of the 1997 International Conference on Networking Protocols, "High Speed, Scalable, and Accurate Implementation of Packet Fair Queuing Algorithms in ATM Networks", Bennett, Stephens and Zhang, Pg. 7-14, (October 1997).

The above-identified reference(s) are listed on the attached PTO Forms 1449 and copies are also enclosed herewith.

This submission, is in no way intended as an admission that the above-cited references constitute prior art under any subsection of 35 U.S.C. § 102. Applicant expressly retains the right to take any actions necessary to remove the above-cited references from the available prior art.

Consideration of the above-identified references in the examination of the present patent application is respectfully requested.

Respectfully submitted,



Date: February 3, 2000

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